

## **ANSI-ASC-C63®** Interpretation Request Form

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<b>Submission Date</b>	Originator Name, Company		
12/6/2014	Ghery Pettit, Intel Corporation		

Standa	d Clause/ Sub clause	Paragraph Figure/ Table	Type (General/	Comment / Inquiry	Subcommittee Response (to be filled in by Subcommittee Chair)
		Table	Technical/ Editorial)		
ANSI C6: 2009	3.4 Section 5.2.2	Paragraph 1	Technical	The first paragraph starts out, "Optionally, for measurements of ac power-line conducted emissions for a tabletop device, a vertical conducting plane or screen, with a size of at least 2 m by 2 m, may be located 40 cm to the rear of the EUT. The vertical conducting plane or screen shall be electrically connected to the reference ground plane at intervals not greater than 1 m along its entire length through low impedance connections (e.g., 3-cm-wide metal straps). The metal wall of a screen room will normally satisfy this requirement"  Can this vertical plane be covered with ferrite tiles or other absorbing material as would be found on the inside of an RF semi-anechoic chamber?	A vertical conducting plane which is used as the reference plane 40 cm away from the EUT cannot be covered with ferrite when measuring conducted emissions per C63.4.  Reason: The vertical reference plane serves as one half of the capacitive coupling path between the power line and the EUT. It is to be void of any dielectric as that would affect the capacitance and the results. So the answer is that ferrite or any other dielectric cannot be on the vertical conducting wall. The best way is to go outside of the chamber and use the exterior conductive wall of the chamber as the vertical conducting plane. At the base of this wall is bonded the 2 m by 2 m (at least) conductive ground plane needed to do the conducted emission test.